

November 2008

FSA2267 / FSA2267A 0.35Ω Low-Voltage Dual-SPDT Analog Switch

Features

- Typical 0.35Ω On Resistance (R_{ON}) for +2.7V supply
- FSA2267A features less than 10µA I_{CCT} current when S Input is lower than V_{CC}
- 0.25Ω maximum R_{ON} flatness for +2.7V supply
- 1.6mm x 2.1mm 10-Lead MicroPak[™] package
- Broad V_{CC} operating range
- Low THD (0.02% typical for 32Ω load)
- High current handling capability (350mA continuous current under 3.3V supply)

Applications

- Cell phone
- PDA
- Portable media player

udavina Infarmatian

Description

The FSA2267 and FSA2267A are Dual Single Pole Double Throw (SPDT) analog switches. The FSA2267 operates from a single 1.65V to 3.6V supply, while the FSA2267A operates from a single 2.3V to 4.3V supply. Each features an ultra-low On Resistance of 0.35Ω at a +2.7V supply and 25°C. Both devices are fabricated with sub-micron CMOS technology to achieve fast switching speeds and designed for break-before-make operation.

FSA2267A features very low quiescent current, even when the control voltage is lower than the V_{CC} supply. This feature services the mobile handset applications very well, allowing for the direct interface with baseband processor general-purpose I/Os.

Ordering into	mation			
Order Number	Top Mark	Eco Status	Package Description	Packing Method
FSA2267L10X	FC	RoHS	10-Lead MicroPak, 1.6 x 2.1mm, JEDEC MO-255	5000 Units on Tape and Reel
FSA2267MUX	FSA 2267	RoHS	10-Lead Molded Small Outline Package (MSOP), JEDEC MO-187, 3.0mm Wide	3000 Units on Tape and Reel
FSA2267AL10X	FD	RoHS	10-Lead MicroPak, 1.6 x 2.1mm, JEDEC MO-255	5000 Units on Tape and Reel
FSA2267AMUX	FSA 2267A	RoHS	10-Lead Molded Small Outline Package (MSOP), JEDEC MO-187, 3.0mm Wide	4000 Units on Tape and Reel

For Fairchild's definition of "green" Eco Status, please visit: <u>http://www.fairchildsemi.com/company/green/rohs_green.html</u>. MicroPak™ is a trademark of Fairchild Semiconductor Corporation







Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only..

Symbol	Parameter	Min.	Max.	Unit
V _{CC}	Supply Voltage	-0.5	+5.5	V
V _S	Switch Voltage ⁽¹⁾	-0.5	V _{CC} + 0.5	V
V _{IN}	Control Input Voltage ⁽¹⁾	-0.5	5.5	V
I _{IK}	Input Diode Current ⁽²⁾	-50		mA
I _{SW}	Switch Current		350	mA
I _{SWPEAK}	Peak Switch Current (Pulsed at 1ms duration, <10% Duty Cycle)		500	mA
T _{STG}	Storage Temperature Range	-65	+150	°C
TJ	Maximum Junction Temperature		+150	°C
TL	Lead Temperature (Soldering, 10 seconds)		+260	°C
	Human Body Model: FSA2267		7500	V
ESD	Human Body Model, JESD22-A114:FSA2267A		7000	V
LOD	Charged Device Model, JESD22-C101: FSA2267/FSA2267A		1000	v

Notes:

- 1. The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.
- 2. Minimums define the acceptable range of current. Negative current should not exceed minimun negative values.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Min.	Max.	Unit
	Supply Voltage			V
V _{CC}	FSA2267	1.65	3.6	V
	FSA2267A	2.3	4.3	
V _{IN}	Control Input Voltage ⁽³⁾	0	V _{CC}	V
V _{SW}	Switch Input Voltage	0	V _{CC}	V
T _A	Operating Temperature	-40	+85	°C

Note:

3. Unused inputs must be held HIGH or LOW. They may not float.

ESD Protection

ESD Performance of the FSA2267/FSA2267A

FSA2267

- HBM all pins 7.0kV
- CDM all pins 1.0kV

FSA2267A

- HBM all pins 7.5kV
- CDM all pins 1.0kV

Human Body Model

Figure 5 shows the schematic representation of the Human Body Model ESD event. Figure 6 is the ideal waveform representation of the Human Body Model. The device is tested to JEDEC: JESD22-A114 Human Body Model.

Charged Device Model

In manufacturing test and handling environments, a more useful model is the Charged Device Model and the FSA2267/FSA2267A has a very good ESD immunity to this model. The device is tested to JEDEC: JESD22-C101 Charged Device Model.

IEC 61000-4-2

The IEC 61000-4-2 standard covers ESD testing and performance of finished equipment and evaluates the equipment in its entirety for ESD immunity. Fairchild Semiconductor has evaluated this device using the IEC 6100-4-2 representative system model depicted in Figure 7.

ESD values measured via the IEC 61000-4-2 evaluation method are influenced by the specific board layout, board size, and many other factors of the manufacturer's product application. Measured system ESD values cannot be guaranteed by Fairchild Semiconductor to exactly correlate to a manufacturer's in-house testing due to these application environment variables. Fairchild Semiconductor has been able to determine that, for ultra-portable applications, an enhanced ESD immunity, relative to the IEC 61000-4-2 specification, can be achieved with the inclusion of a 100 Ω -series resistor in the V_{CC} supply path to the analog switch (see Figure 8). Typical improvements of between 3-6kV of ESD immunity (I/O to GND) have been measured with the inclusion of the resistor with the IEC 61000-4-2 representative model. For more information on ESD testing methodologies, please refer to:

AN-6019 Fairchild Analog Switch Products ESD Test Methodology Overview

http://www.fairchildsemi.com/an/AN/AN-6019.pdf.

Additional ESD Test Conditions

For information regarding test methodologies and performance levels, please contact Fairchild Semiconductor.















FSA2267 DC Electrical Characteristics

All typical values are at 25°C unless otherwise specified.

Symbol	Parameter	Parameter Conditions (V _C		٦	_ = +25	°C	T _A = - +8	Units	
				Min.	Тур.	Max.	Min.	Max.	
			2.7 to 3.6				2.0		
V	Input Voltage High		2.3 to 2.7				1.7		V
ЧН	input voltage riigh		1.65 to 1.95				0.65 V _{CC}		v
			2.7 to 3.6					0.8	
Vu	Input Voltage Low		2.3 to 2.7					0.7	V
۲L	input voluge Low		1.65 to 1.95					0.35 V _{CC}	·
I _{IN}	Control Input Leakage	V_{IN} = 0V to V_{CC}	1.65 to 3.6				-0.5	0.5	μA
		nA = 0.3V, 3.3V, nB ₀ or nB ₁ = 0.3V, 3.3V or floating	3.6	-5.0		5.0	-50	50	
I _{NO(OFF)} , I _{NC(OFF)}	Off-Leakage Current of Port nB_0 and nB_1	$nA = 0.3V, 2.4V, nB_0 \text{ or } nB_1$ = 0.3V, 2.4V or floating	2.7	-5.0		5.0	-50	50	nA
		nA = 0.3V, 1.65V, nB ₀ or nB ₁ = 0.3V, 1.65V or floating	1.95	-5.0		5.0	-50	50	
	On Leakage Current of Port 1A and 2A	nA = 0.3V, 3.3V, nB ₀ or nB ₁ = 0.3V, 3.3V or floating	3.6	-5.0		5.0	-50	50	
I _{A(ON)}		$nA = 0.3V, 2.4V, nB_0 \text{ or } nB_1$ = 0.3V, 2.4V or floating	2.7	-5.0		5.0	-50	50	nA
		nA = 0.3V, 1.65V, nB ₀ or nB ₁ = 0.3V, 1.65V or floating	1.95	-5.0		5.0	-50	50	
		I _{OUT} = 100mA, nB ₀ or nB ₁ = 0V, 0.7V, 2.0V, 2.7V	2.7		0.35			0.60	
R _{ON}	Switch On Resistance ⁽⁴⁾ See Figure 9	I _{OUT} = 100mA, nB ₀ or nB ₁ = 0V, 0.7V, 1.6V, 2.3V	2.3		0.45			0.75	Ω
		I _{OUT} = 100mA, nB ₀ or nB ₁ = 0.8V	1.65		1.0			3.9	
			2.7		0.040			0.075	
ΔR_{ON}	On Resistance Matching Between Channels ⁽⁵⁾	I _{OUT} = 100mA, nB ₀ or nB ₁ = 0 7V	2.3		0.040			0.080	Ω
		••	1.65		0.1				
			2.7					0.25	
R _{FLAT(ON)}	On Resistance Flatness ⁽⁶⁾	$I_{OUT} = 100$ mA, nB ₀ or nB ₁ = 0V to V _{CC}	2.3					0.3	Ω
			1.65		0.3				
I _{CC}	Quiescent Supply Current	V_{IN} = 0V or V_{CC} , I_{OUT} = 0A	3.6	-100		100	-500	500	nA

FSA2267/FSA2267A 0.35Ω Low-Voltage Dual-SPDT Analog Switch

Notes:

- 4. On resistance is determined by the voltage drop between A and B pins at the indicated current through the switch.
- 5. $\Delta R_{ON} = R_{ONmax} R_{ONmin}$ measured at identical V_{CC}, temperature, and voltage.
- Flatness is defined as the difference between the maximum and minimum value of R_{ON} over the specified range of conditions.

FSA2267A DC Electrical Characteristics

All typical values are at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V _{cc}	Тд	_ = +25	°C	T _A = - +8	-40 to 5°C	Units
			(V)	Min.	Тур.	Max.	Min.	Max.	
			3.6 to 4.3				1.7		
V_{IH}	Input Voltage High		2.7 to 3.6				1.5		V
			2.3 to 2.7				1.4		
			3.6 to 4.3					0.7	
V _{IL}	Input Voltage Low		2.7 to 3.6					0.5	V
			2.3 to 2.7					0.4	
I _{IN}	Control Input Leakage	V_{IN} = 0V to V_{CC}	2.3 to 4.3				-0.5	0.5	μA
		nA = 0.3V, 4.0V, nB ₀ or nB ₁ = 4.0V, 0.3V or floating	4.3	-10.0		10.0	-100	100	
I _{NO(OFF)} , I _{NC(OFF)}	Off-Leakage Current of Port nB_0 and nB_1	nA = 0.3V, 3.3V, nB ₀ or nB ₁ = 0.3V, 3.3V or floating	3.6	-5.0		5.0	-50	50	nA
	,	nA = 0.3V, 2.4V, nB ₀ or nB ₁ = 0.3V, 2.4V or floating	2.7	-5.0		5.0	-50	50	
	On Leakage Current of Port 1A and 2A	nA = 0.3V, 4.0V, nB ₀ or nB ₁ = 0.3V, 4.0V or floating	4.3	-20.0		20.0	-200	200	
I _{A(ON)}		nA = 0.3V, 3.3V, nB ₀ or nB ₁ = 0.3V, 3.3V or floating	3.6	-5.0		5.0	-50	50	nA
		nA = 0.3V, 3.3V, nB ₀ or nB ₁ = 0.3V, 3.3V or floating	2.7	-5.0		5.0	-50	50	
	Switch On Resistance ⁽⁷⁾	I _{OUT} = 100mA, nB ₀ or nB ₁ = 0V, 0.7V, 3.6V, 4.3V	4.3		0.35			0.6	
R _{ON}		I _{OUT} = 100mA, nB ₀ or nB ₁ = 0V, 0.7V, 2.3V, 3.0V	3.0		0.35			0.6	Ω
		I _{OUT} = 100mA, nB ₀ or nB ₁ = 0V, 0.7V, 2.0V, 2.7V	2.7		0.35			0.6	
		I_{OUT} = 100mA, nB ₀ or nB ₁ = 0.8V	1.65		1.0				
			4.3		0.04			0.075	
ARau	On Resistance Matching	$I_{a,v=} = 100 \text{mA} \text{ nB}_{a} \text{ or nB}_{v} = 0.7 \text{V}$	3.0		0.04			0.075	0
AINON	See Figure 10	1001 - 10000, $100 - 0.7$	2.7		0.04			0.075	22
			1.65		0.1				
			4.3		0.15			0.25	
	On Resistance	I_{OUT} = 100mA, nB ₀ or nB ₁ = 0V	3.0		0.15			0.25	Ω
TLAI (UN)	Flatness	to V _{CC}	2.7		0.15			0.25	
			1.65		0.3				
I _{CC}	Quiescent Supply Current	V_{IN} = 0V or V_{CC} , I_{OUT} = 0A	4.3	-100	80	100	-500	500	nA
Ісст	Increase in loc per Input	V _{IN} = 1.8V	43		7.0	10.0		15.0	μА
-001		V _{IN} = 2.6V			0.5	2.0		7.0	μΑ

FSA2267/FSA2267A 0.35Ω Low-Voltage Dual-SPDT Analog Switch

Notes:

- 7. On resistance is determined by the voltage drop between A and B pins at the indicated current through the switch.
- 8. $\Delta R_{ON} = R_{ONmax} R_{ONmin}$ measured at identical V_{CC}, temperature, and voltage.
- 9. Flatness is defined as the difference between the maximum and minimum value of R_{ON} over the specified range of conditions.

FSA2267 AC Electrical Characteristics

All typical values are at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V _{cc}	T _A = +25°C		T _A = - +8	40 to 5°C	Units	Figure Number	
-			(V)	Min.	Тур.	Max.	Min.	Max.		
-			2.7 to 3.6		30.0	38.0		42.0		
t _{ON}	Turn-On Time	n Time $B_0 \text{ or } nB_1 = 1.5V,$ B ₁ = 500, C ₂ = 35 pE	2.3 to 2.7		29.0	37.0		40.0	ns	Figure 11
			1.65 to 1.95		27.0	35.0		38.0		
			2.7 to 3.6		13.0	16.0		18.0		
t _{OFF}	Turn-Off Time	$nB_0 \text{ or } nB_1 = 1.5V,$ $B_1 = 500$ $C_1 = 35 \text{ pF}$	2.3 to 2.7		14.0	18.0		20.0	ns	Figure 11
			1.65 to 1.95		15.0	21.0		25.0		
	-		2.7 to 3.6		17.0		2.0			
t _{BBM}	Break-Before- Make Time	$nB_0 \text{ or } nB_1 = 1.5V,$ $B_1 = 500$ $C_1 = 35 \text{ pF}$	2.3 to 2.7		15.0		2.0		ns	Figure 12
			1.65 to 1.95		12.0		2.0			
		C_L = 100 pF, V _{GEN} = 0V, R _{GEN} = 0Ω	2.7 to 3.6		9.0					
Q	$ \begin{array}{l} \mbox{Charge Injection} \\ \mbox{C}_{L} = 100 \mbox{ pF}, V_{GEN} = 0V, \\ \mbox{R}_{GEN} = 0\Omega \end{array} $	C_L = 100 pF, V _{GEN} = 0V, R _{GEN} = 0Ω	2.3 to 2.7		9.0				pC	Figure 14
		C_L = 100 pF, V _{GEN} = 0V, R _{GEN} = 0Ω	1.65 to 1.95		9.0					
		$f = 100$ kHz, $R_L = 50\Omega$, $C_L = 5pF$	2.7 to 3.6		-80.0					
OIRR	Off Isolation		2.3 to 2.7		-80.0				dB	Figure 13
		(oldy)	1.65 to 1.95		-80.0					
			2.7 to 3.6		-80.0					
Xtalk	Crosstalk	$f = 100$ kHz, $R_L = 50\Omega$, $C_L = 5$ pF (Stray)	2.3 to 2.7		-80.0				dB	Figure 13
		(oldy)	1.65 to 1.95		-80.0					
BW	-3db Bandwidth	R _L = 50Ω	1.65 to 3.6		45.0				MHz	Figure 16
		$R_{L} = 32\Omega, V_{IN} = 2V_{pk-pk},$ f = 20Hz to 20kHz	2.7 to 3.6		0.024					
THD	Total Harmonic Distortion	$R_{L} = 32\Omega, V_{IN} = 1.5V_{pk-pk},$ f = 20Hz to 20kHz	2.3 to 2.7		0.015				%	Figure 17
		$R_{L} = 32\Omega, V_{IN} = 1.2V_{pk-pk},$ f = 20Hz to 20kHz	1.65 to 1.95		0.35					

FSA2267A AC Electrical Characteristics

All typical value are at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	Conditions V _{CC} T _A = +25°		5°C	T _A = - +8	-40 to 5°C	Units	Figure	
				Min.	Тур.	Max.	Min.	Max.		Number
			3.6 to 4.3		37.0	46.0		48.0		
+	T 0 T	$nB_0 \text{ or } nB_1 = 1.5V,$	2.7 to 3.6		37.0	50.0		57.0		Figure 11
LON	Tum-On Time	$R_{L} = 50\Omega, C_{L} = 35pF$	2.3 to 2.7		60				ns	Figure 11
		1.65		570						
			3.6 to 4.3		15.0	23.0		25.0		
	Turn Off Times	$nB_0 \text{ or } nB_1 = 1.5V,$	2.7 to 3.6		16.0	30.0		30.0		Figure 11
LOFF	rum-on nme	$R_L = 50\Omega, C_L = 35pF$	2.3 to 2.7		50.0				ns	Figure 11
			1.65		500					
			3.6 to 4.3		8.0		2.0			
t _{BBM}	Break-Before-	Before- $nB_0 \text{ or } nB_1 = 1.5V$, ime $B_1 = 50Q$, $C_2 = 35pF$			8.0		2.0		ns	Figure 12
	Make Time	NL = 3032, OL = 3501	2.3 to 2.7		8.0		2.0			
		C_L = 100 pF, V_{GEN} = 0V, R _{GEN} = 0 Ω	3.6 to 4.3		24.0					
Q	Charge Injection	C_L = 100 pF, V_{GEN} = 0V, R _{GEN} = 0 Ω	2.7 to 3.6		24.0				рС	Figure 14
		C_L = 100 pF, V_{GEN} = 0V, R _{GEN} = 0 Ω	2.3 to 2.7		24.0					
			3.6 to 4.3		-75.0					
OIRR	Off Isolation	f = 100kHz, $R_L = 50\Omega$, $C_L = 5pF$ (Strav)	2.7 to 3.6		-75.0				dB	Figure 13
		(ondy)	2.3 to 2.7		-75.0					
			3.6 to 4.3		-70.0					
Xtalk	Crosstalk	f = 100kHz, $R_L = 50\Omega$, $C_L = 5pF$ (Stray)	2.7 to 3.6		-70.0				dB	Figure 13
		(ondy)	2.3 to 2.7		-70.0					
BW	-3db Bandwidth	R _L = 50Ω	2.3 to 4.3		45.0				MHz	Figure 16
		$R_{L} = 32\Omega, V_{IN} = 2V_{pk-pk},$ f = 20Hz to 20kHz	3.6 to 4.3		0.02					
THD	Total Harmonic Distortion	$R_{L} = 32\Omega, V_{IN} = 1.5V_{pk-pk},$ f = 20Hz to 20kHz	2.7 to 3.6		0.02				%	Figure 17
		$R_{L} = 32\Omega, V_{IN} = 1.2V_{pk-pk},$ f = 20Hz to 20kHz	2.3 to 2.7		0.02					

Capacitance

Symbol	Parameter	Conditions	V _{CC} (V)	TA	_ = +25	°C	T _A = - +8	-40 to 5°C	Units	Figure Number
				Min.	Тур.	Max.	Min.	Max.		
C _{IN}	Control Pin Input Capacitance	f = 1Mhz	0.0		1.5				pF	Figure 15
C _{OFF}	B Port Off Capacitance	f = 1Mhz	3.3		30.0				pF	Figure 15
C _{ON}	A Port On Capacitance	f = 1Mhz	3.3		126				pF	Figure 15



FSA2267/FSA2267A 0.35Ω Low-Voltage Dual-SPDT Analog Switch









FSA2267/FSA2267A 0.35Ω Low-Voltage Dual-SPDT Analog Switch



* EZSWITCH™ and FlashWriter® are trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELABILITY, FUNCTION, OR DESIGN, FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice bybuying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms									
Datasheet Identification	Product Status	Definition							
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.							
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.							
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.							
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.							

Rev. 137 www.fairchildsemi.com